FIG. 1

SECTOR FIELD LAYER

					Œ	RECORDING FIELD	G FIELD			
SEGMENTNAME	HEADER FIELD	MIRROR FIELD	GAP FIELD	GUARD 1 VFO3 FIELD FIELD	VFO3 FIELD	PS FIELD	DATA FIELD	PA3 FIELD	GUARD 2 FUFFER FIELD FIELD	FUFFER FIELD
BYTE COUNT	128	2	10 +J/16	20+K	35	3	2418	-	55-K	25 -J/16

J: 0-15 K: 0-7

FIG. 2

HEADER FIELD LAYER

	HEADE	R 1 F	IELD		١	HEADE	R 2 F	IELD	
VFO1	AM	PID1	ID1	PA1	VFO2	AM	PID2	ID2	PA2
36	3	4	2	1	8	3	4	2	1
L	<u> </u>	<u> </u>	<u> </u>						

	HEADE	R 3 F	IELD		<u> </u>	HEADE	R 4 F	TELD	
VFO1	AM	PID3	ID3	PA1	VFO2	AM	PID4	ID4	PA2
36	3	4	2	1	8	3	4	2	1

FIG. 3

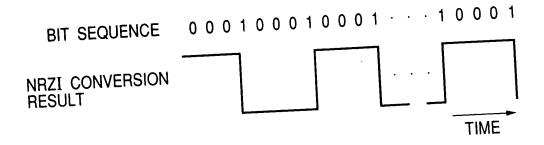


FIG. 4

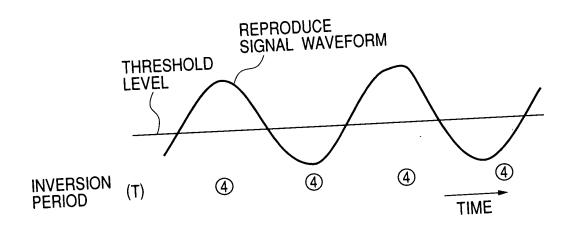
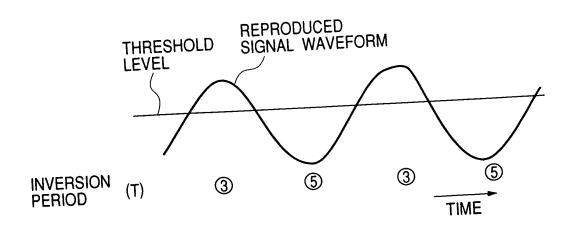


FIG. 5



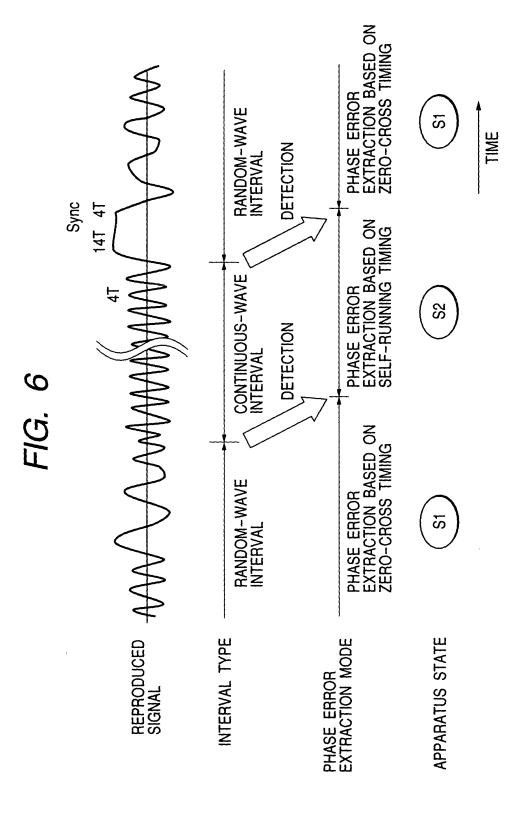


FIG. 7

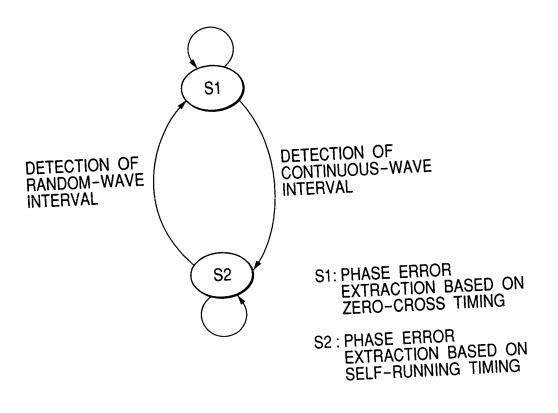


FIG. 8

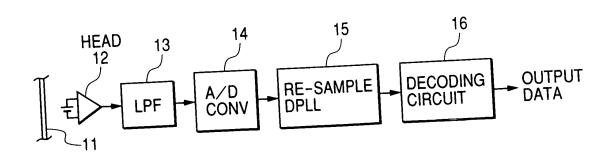


FIG. 9

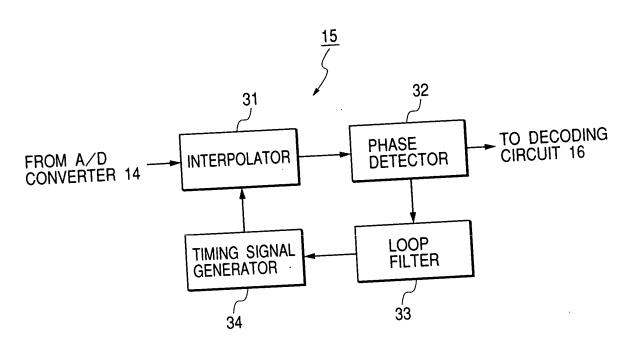


FIG. 10

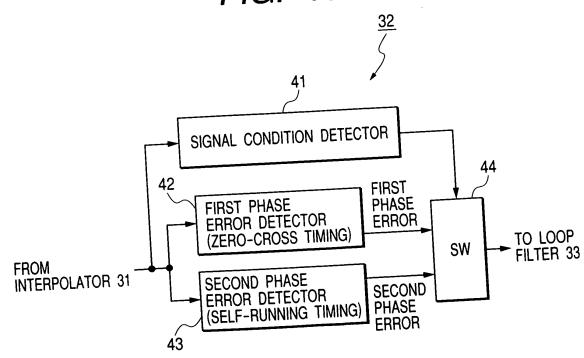


FIG. 11

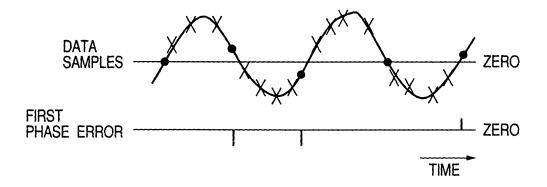
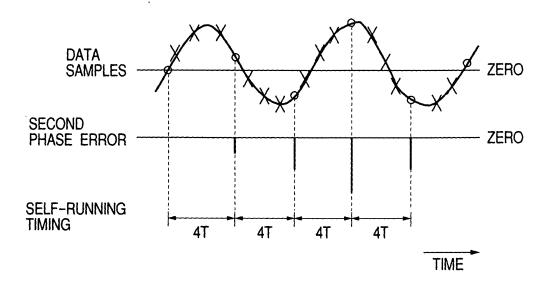
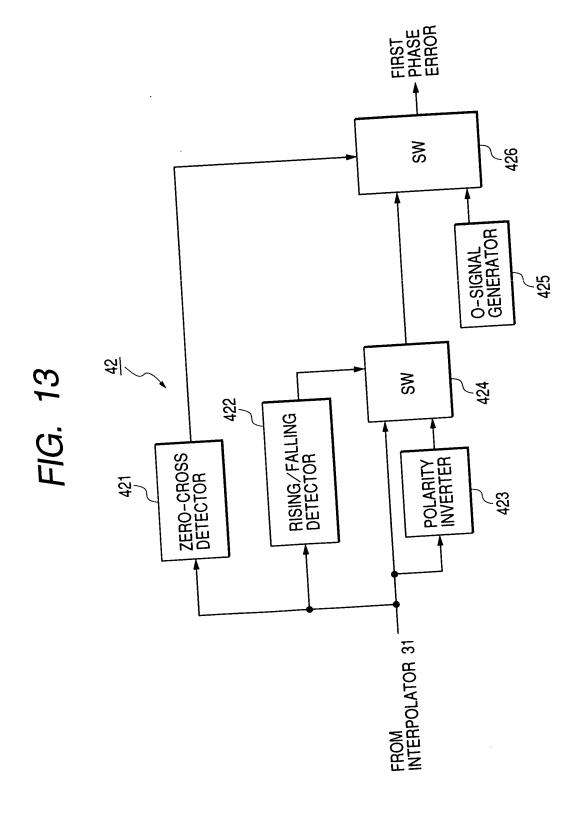
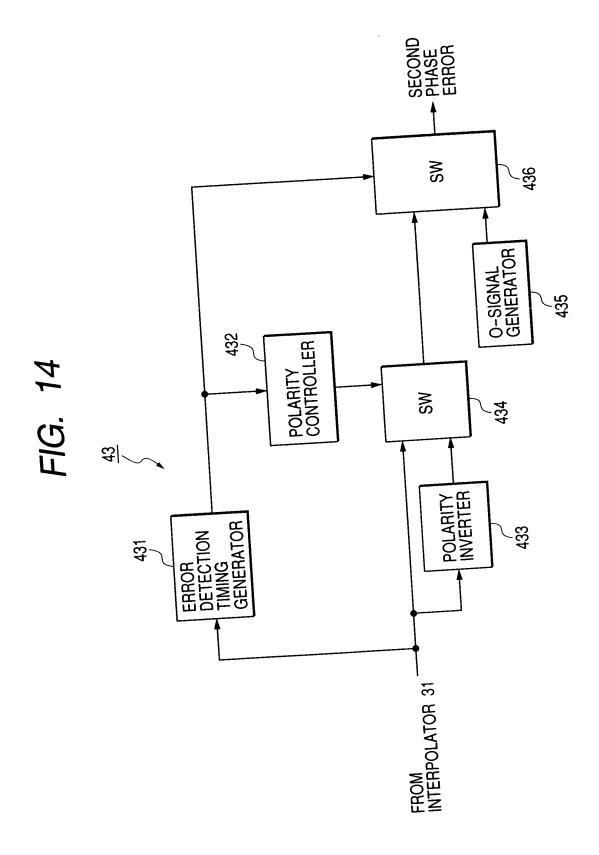
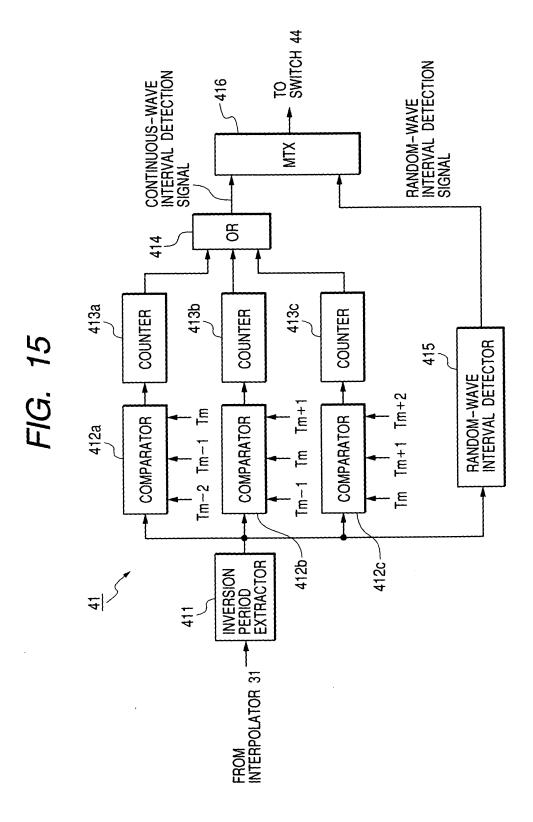


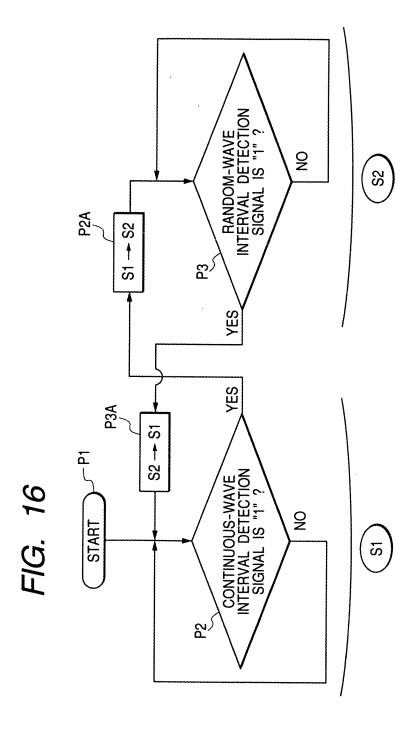
FIG. 12











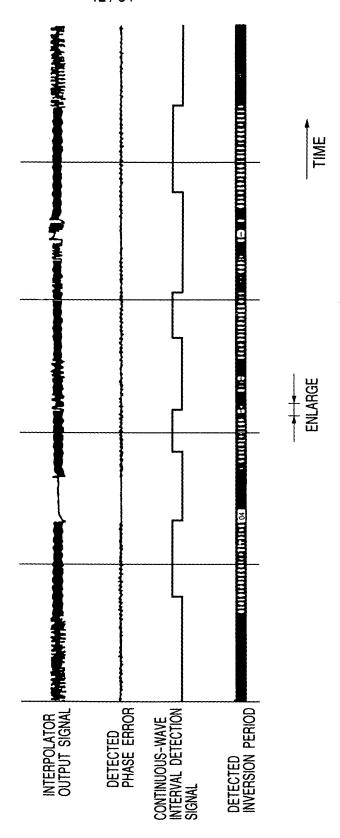
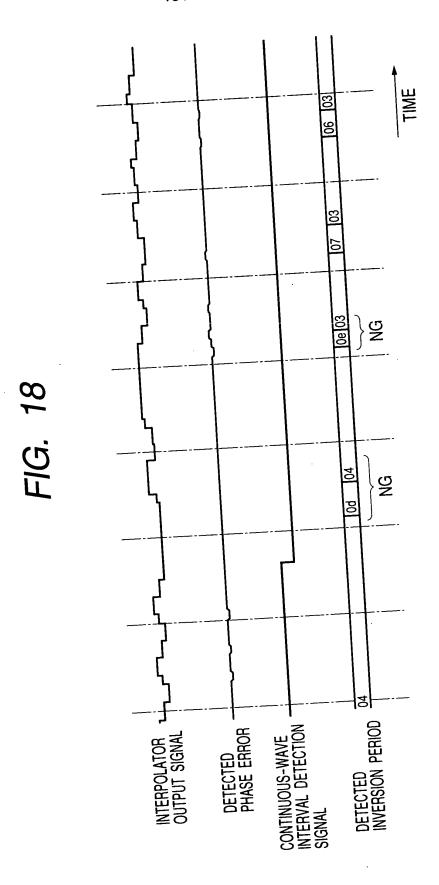


FIG. 17



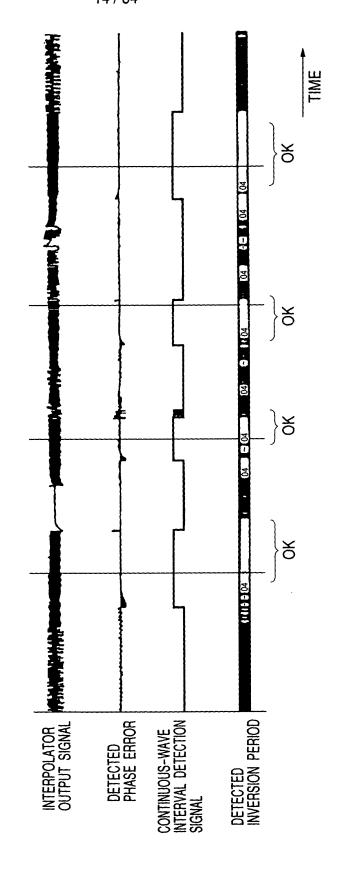
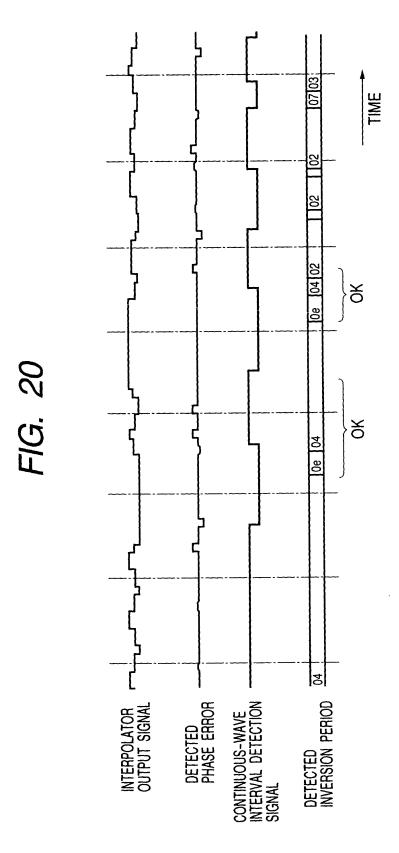


FIG. 19



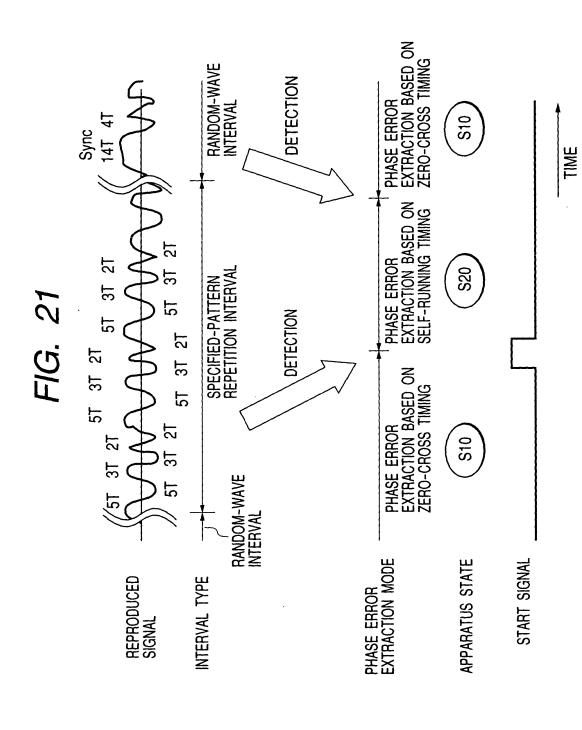


FIG. 22

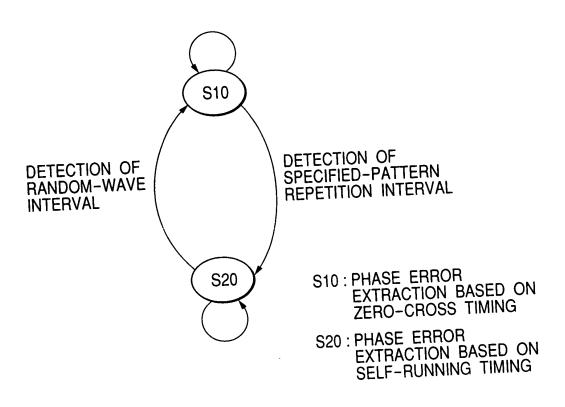


FIG. 23

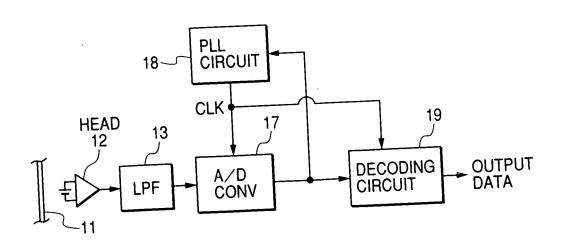


FIG. 24

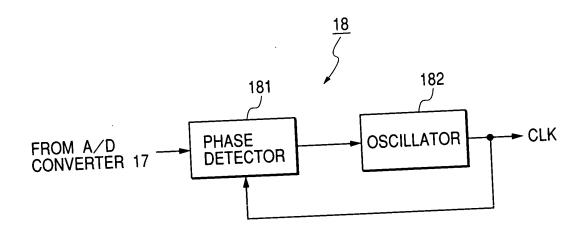
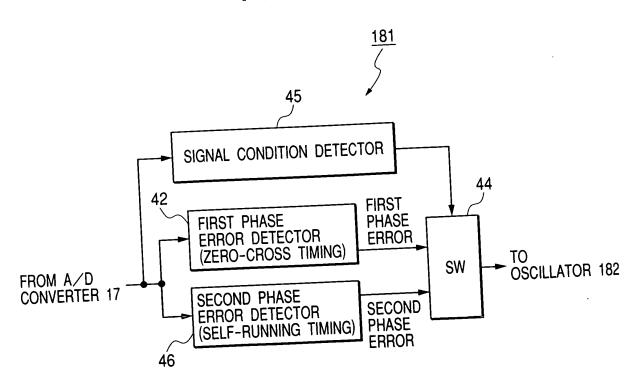
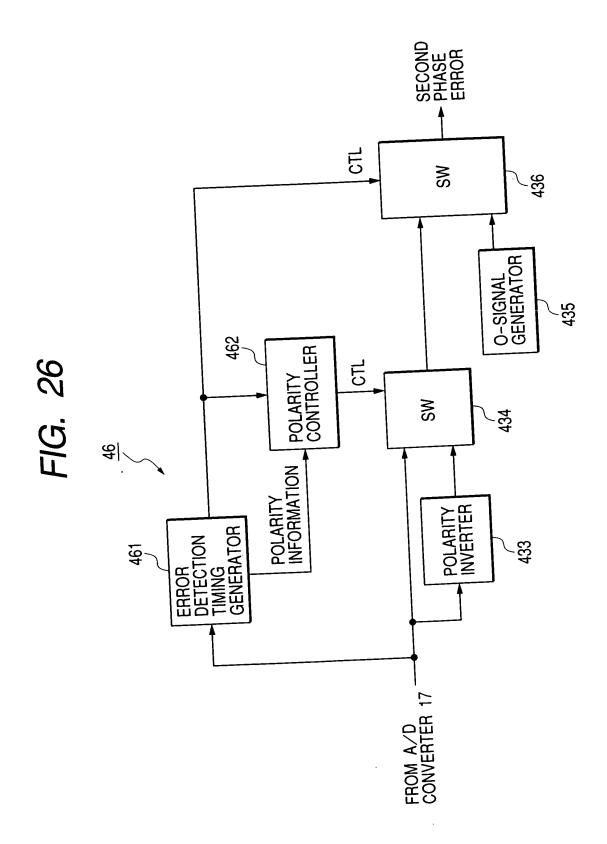
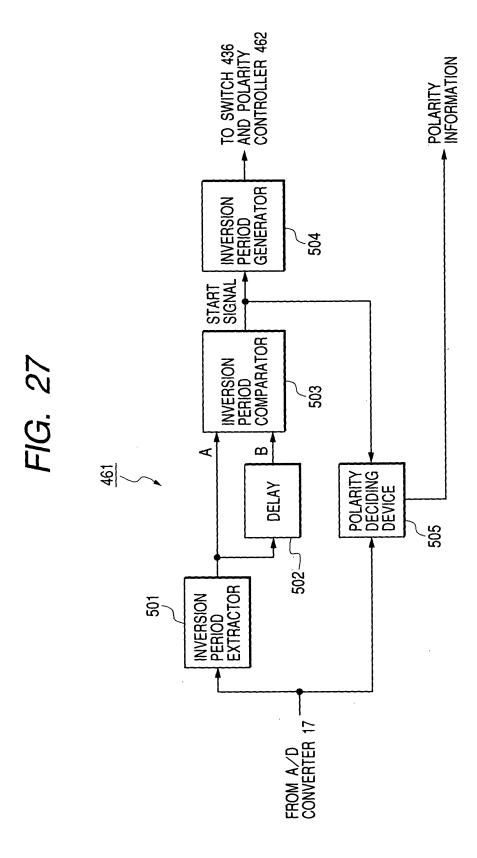
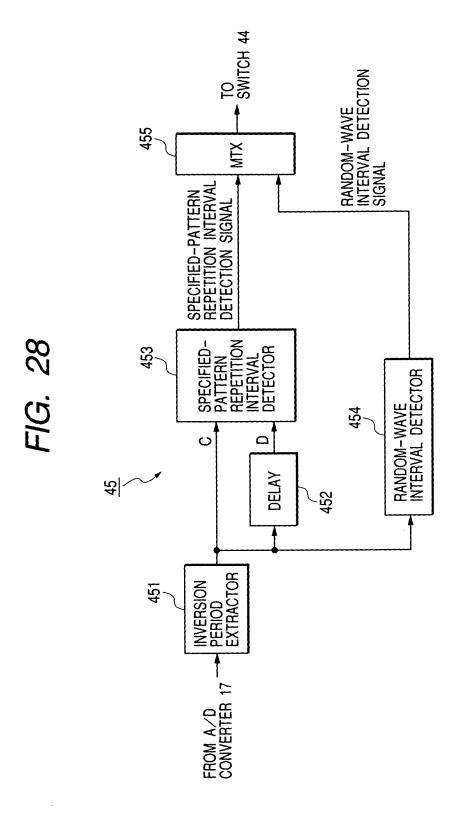


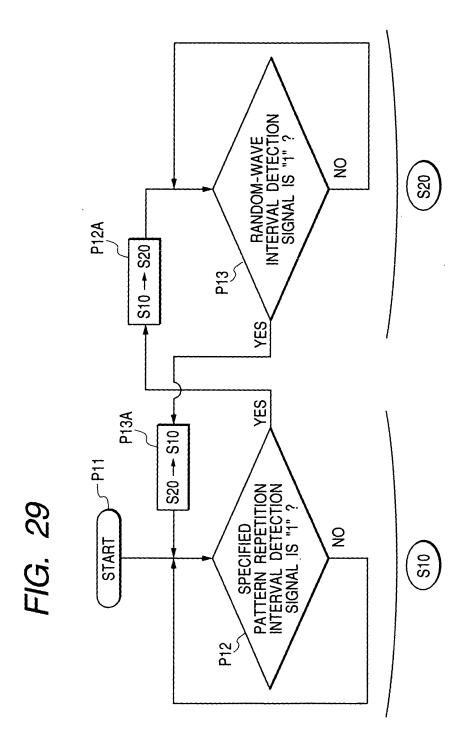
FIG. 25



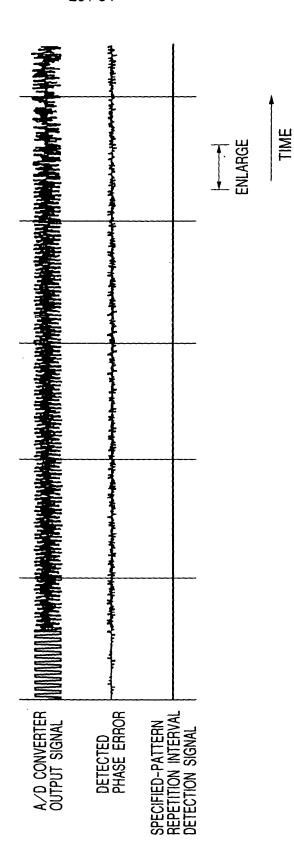


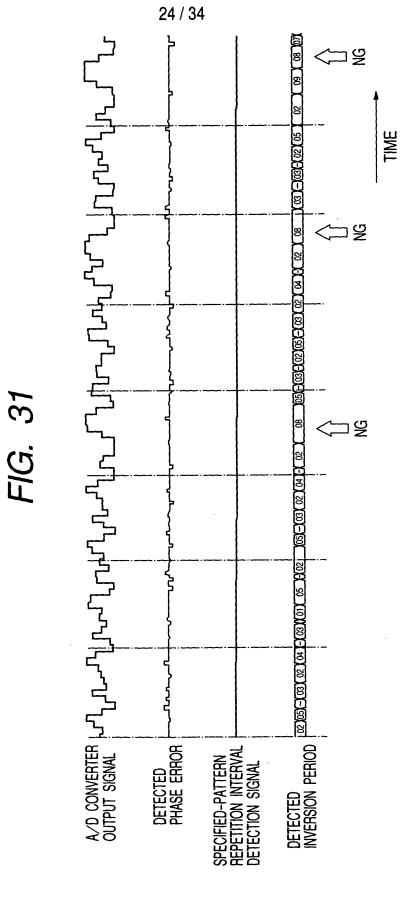


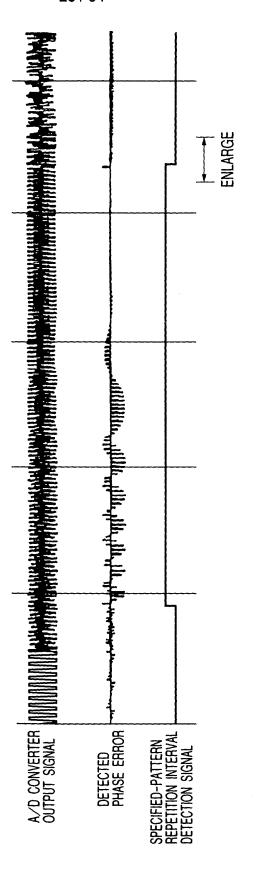












TIME

FIG. 32

<⇒ ¥ FIG. 33 <⇒ ¥ 02 (05)(03 (05)(03 (05) (05)(09 (05) (05)(09 (05)(05) DETECTED PHASE ERROR SPECIFIED-PATTERN REPETITION INTERVAL DETECTION SIGNAL DETECTED INVERSION PERIOD

26/34

TWE

FIG. 34

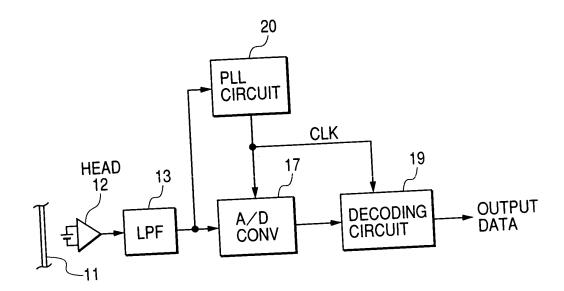
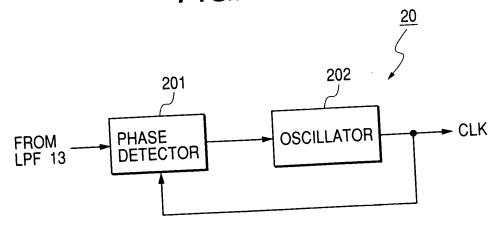


FIG. 35



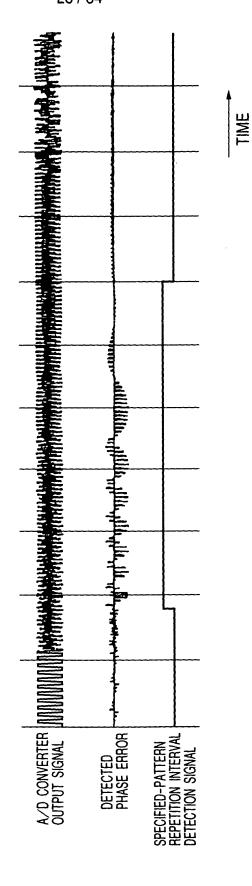


FIG. 36

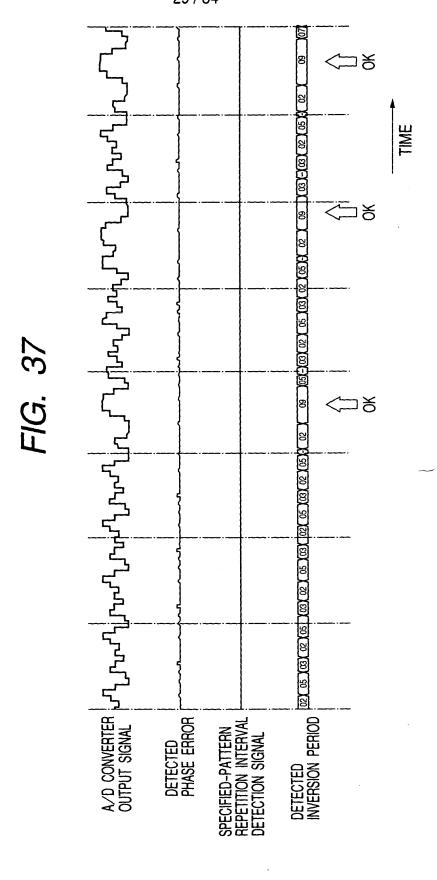


FIG. 38

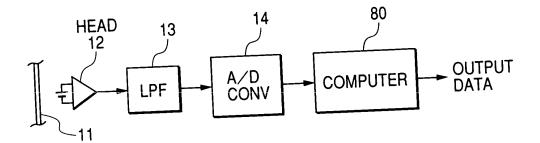
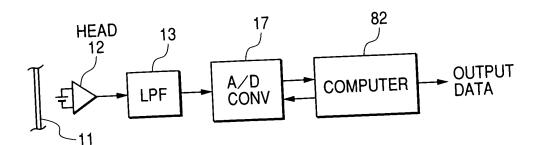


FIG. 39



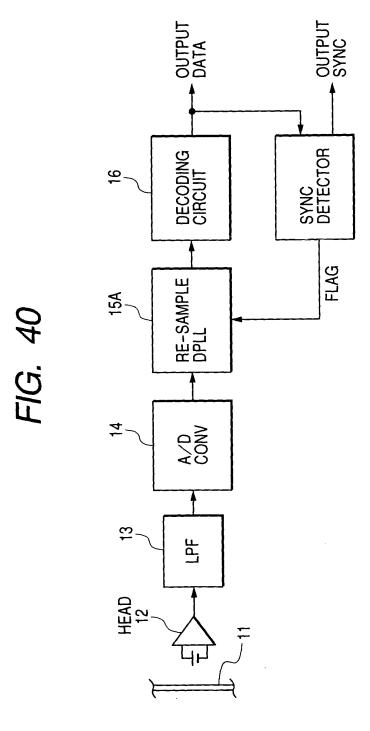


FIG. 41

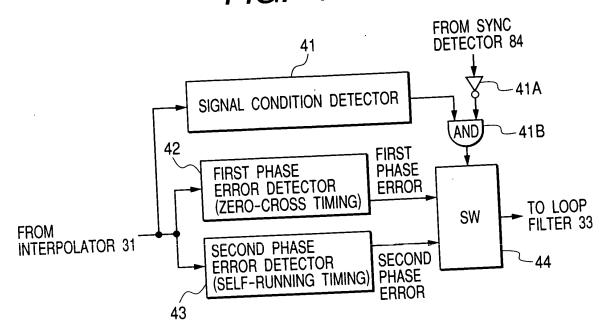


FIG. 42

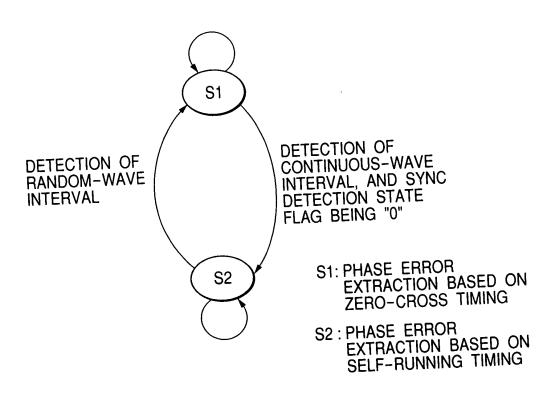


FIG. 43

33

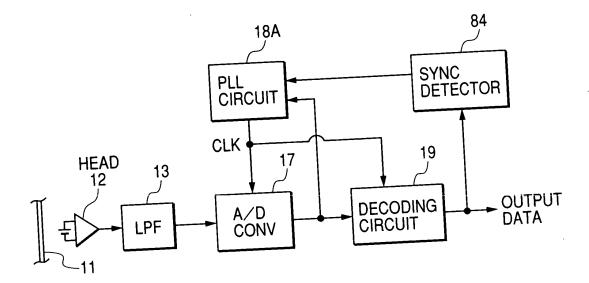


FIG. 44

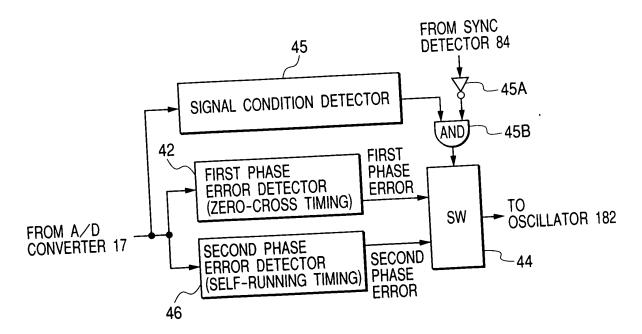


FIG. 45

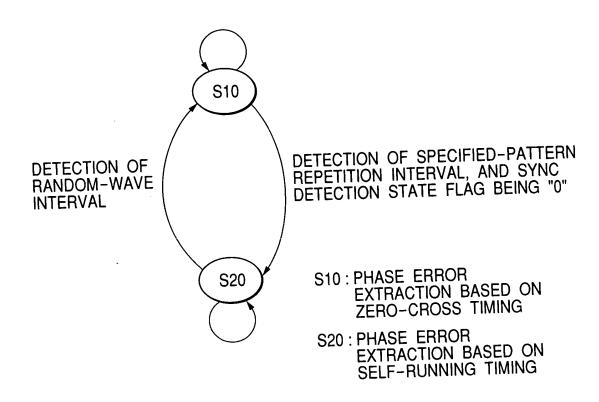


FIG. 46

